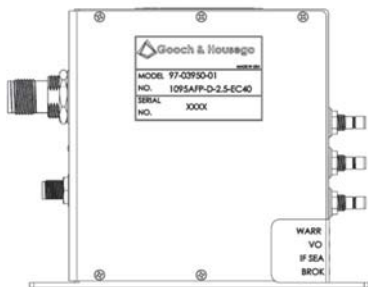




97-03950-XX Driver Specifications

40-149MHz, 0-3W RF Power, External Clock



Specifications

Parameter	Performance
Center frequency (Fc)	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability*	+/- 5 ppm over temp
Frequency accuracy*	+/- 5 ppm
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (at mounting tab). Unit will operate to 0°C, non-condensing environment
External Clock Frequency	10MHz – 250MHz
External Clock Level	1V – 3V Peak to Peak

* Recommended External Clock Characteristic Performance Parameters.

Table A

Part Number	Center Freq (MHz)	Modulation	RF Power (Watts)	Ext Clock Freq (MHz)	Model
97-03950-01	95	Digital	2.5	40	1095AFP-D-2.5-EC40
97-03950-02	80	Combined	1	80	1080AFP-AD-1.0-EC80
97-03950-03	40	Combined	0.4	10	1040AFP-AD-0.4-EC10
97-03950-04	40	Combined	0.4	40	1040AFP-AD-0.4-EC40
97-03950-05	80	Combined	2.5	10	1080AFP-AD-2.5-EC10
97-03950-06	80	Analog	1	10	1080AFP-A-1.0-EC10
97-03950-07	80	Analog	3	10	1080AFP-A-3.0-EC10
97-03950-08	80	Combined	0.4	10	1080AFP-AD-0.4-EC10
97-03950-09	80	Combined	1	10	1080AFP-AD-1.0-EC10

Document

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Control

Table B

