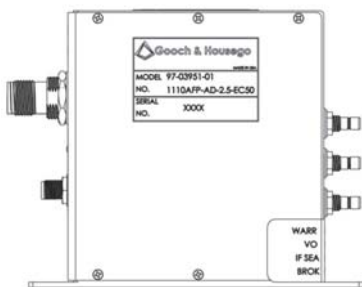


97-03951-XX Driver Specifications

100-199MHz, 0-3W RF Power, External Clock

Table A



Specifications

Parameter	Performance
Center frequency (Fc)	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability*	+/- 5 ppm over temp
Frequency accuracy*	+/- 5 ppm
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (at mounting tab). Unit will operate to 0°C, non-condensing environment
External Clock Frequency	10MHz – 250MHz
External Clock Level	1V – 3V Peak to Peak

Part Number	Center Freq (MHz)	Modulation	RF Power (Watts)	Ext Clock Freq (MHz)	Model
97-03951-01	110	Combined	2.5	50	1110AFP-AD-2.5-EC50
97-03951-02	100	Combined	2.5	50	1100AFP-AD-2.5-EC50
97-03951-03	175	Digital	2.5	175	1175AFP-D-2.5-EC175
97-03951-04	150	Combined	2.5	10	1150AFP-AD-2.5-EC10
97-03951-05	110	Combined	2.5	10	1110AFP-AD-2.5-EC10
97-03951-06	150	Combined	1	10	1150AFP-AD-1.0-EC10
97-03951-07	150	Combined	2	10	1150AFP-AD-2.0-EC10

* Recommended External Clock Characteristic Performance Parameters.

Table B

