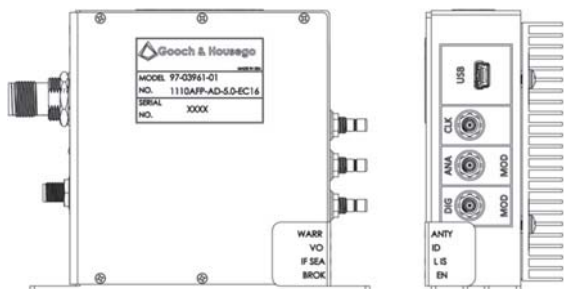




97-03961-XX Driver Specifications 100-199MHz, 3-7W RF Power, External Clock, Heatsink

Table A

Part Number	Freq (MHz)	Modulation	RF Power (Watts)	Clock Freq (MHz)	Model
97-03961-01	110	Combined	5	16	1110AFP-AD-5.0-EC16
97-03961-02	165	Digital	4.5	10	1165AFP-D-4.5-EC10
97-03961-03	110	Combined	3	25	1110AFP-AD-3.0-EC25
97-03961-04	165	Combined	4.5	10	1165AFP-AD-4.5-EC10
97-03961-05	110	Combined	3	110	1110AFP-AD-3.0-EC110



Specifications

Parameter	Performance
Center frequency	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability*	+/- 5 ppm over temp
Frequency accuracy*	+/- 5 ppm
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (at mounting tab). Unit will operate to 0°C, non-condensing environment
External Clock Frequency	10MHz – 250MHz
External Clock Level	1V – 3V Peak to Peak

Document

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Control

* Recommended External Clock Characteristic Performance Parameters.

Table B

