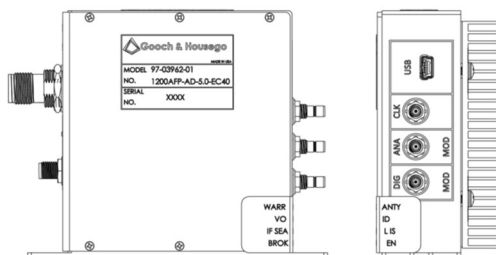




97-03962-XX Driver Specifications

200-350MHz, 3-7W RF Power, Internal Clock



Specifications

Parameter	Performance
Center frequency (Fc)	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time Analog	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Rise / Fall time Digital	25 nsec max
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability	+/- 5 ppm over temp
Frequency accuracy	+/- 5 ppm
Jitter	0.3 psec typ.
Phase Noise	-105 dBc/Hz at 10kHz offset
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (referenced to mounting tab) Unit will operate down to 0°C, in a non-condensing environment

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Table A

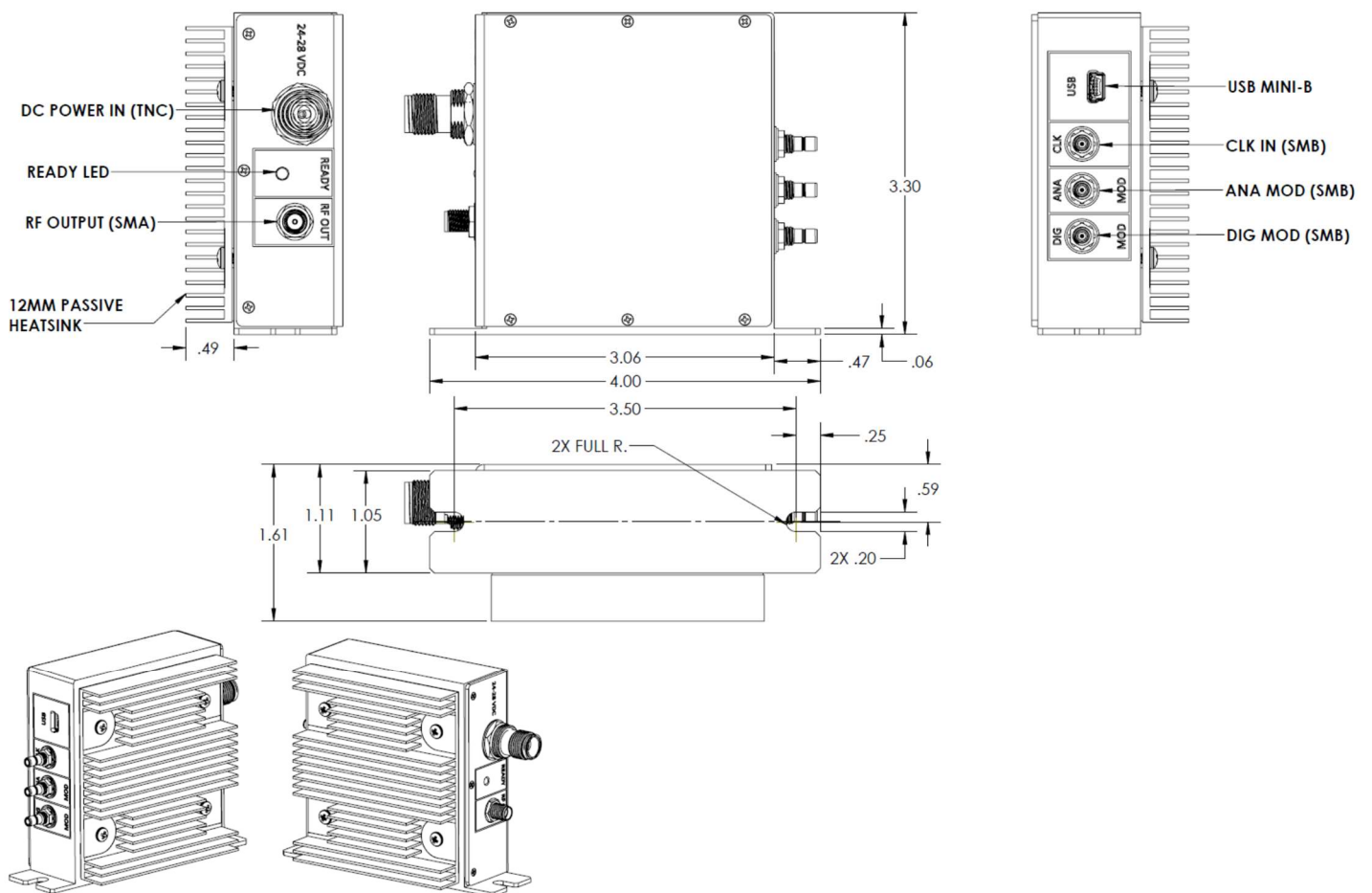
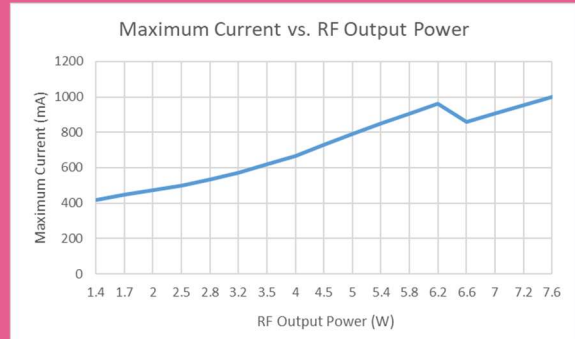
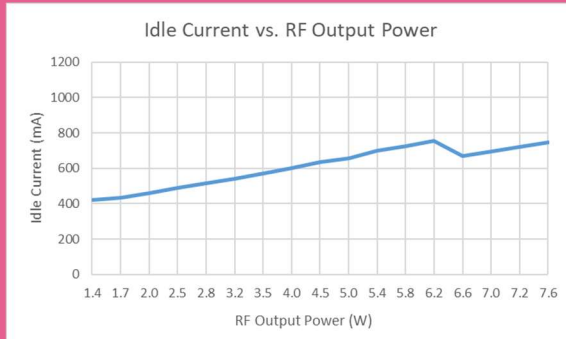
Part Number	Freq (MHz)	Modulation	RF Power (Watts)	Clock Freq (MHz)	Model
97-03962-01	200	Combined	5	40	1200AFP-AD-5.0-EC40
97-03962-02	250	Combined	6.6	10	1250AFP-AD-6.6-EC10
97-03962-03	304	Combined	5	76	1304AFP-AD-5.0-EC76
97-03962-04	300	Combined	5	10	1300AFP-AD-5.0-EC10
97-03962-05	220	Combined	3	10	1220AFP-AD-3.0-EC10
97-03962-06	250	Combined	6.6	25	1250AFP-AD-6.6-EC25
97-03962-07	300	Combined	5	50	1300AFP-AD-5.0-EC50

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Table B



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