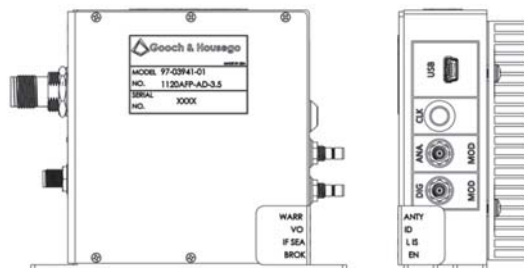




97-03940-XX Driver Specifications

40-149MHz, 3-7W RF Power, Internal Clock, Heatsink



Specifications

Parameter	Performance
Center frequency (Fc)	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time Analog	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Rise / Fall time Digital	25 nsec max
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability	+/- 5 ppm over temp
Frequency accuracy	+/- 5 ppm
Jitter	0.3 psec typ.
Phase Noise	-105 dBc/Hz at 10kHz offset
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (referenced to mounting tab) Unit will operate down to 0°C, in a non-condensing environment

Table A

Part Number	Freq Center (MHz)	Modulation	RF Power (Watts)	Model
97-03940-01	80	Combined	5	1080AFP-AD-5.0
97-03940-02	80	Digital	5	1080AFP-D-5.0
97-03940-03	80	Combined	6	1080AFP-AD-6.0
97-03940-04	80	Analog	5	1080AFP-A-5.0
97-03940-05	80	Combined	3.5	1080AFP-AD-3.5
97-03940-06	80	Analog	3.5	1080AFP-A-3.5
97-03940-07	80	Digital	3.5	1080AFP-D-3.5
97-03940-08	80	Combined	4	1080AFP-AD-4.0
97-03940-09	80	Combined	3	1080AFP-AD-3.0
97-03940-10	80	Digital	6	1080AFP-D-6.0
97-03940-11	80	Combined	8	1080AFP-AD-8.0
97-03940-12	85	Digital	6	1085AFP-D-6.0
97-03940-13	80	Analog	5.5	1080AFP-A-5.5

Document

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Control

Table B

