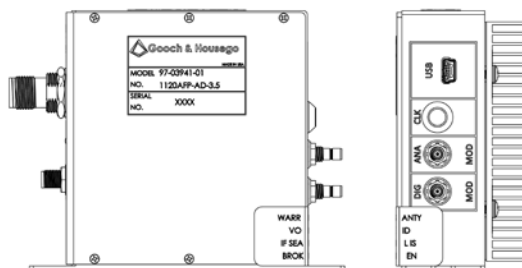




97-03941-XX Driver Specifications

100-199MHz, 3-7W RF Power, Internal Clock



Specifications

Parameter	Performance
Center frequency (Fc)	See Table A
RF output power	See Table A
RF output power stability	+/- 5% over heat sink temperature range
Input voltage +Vcc	24VDC +/- 0.5V measured at input while driver in operation
Supply Current	See Table B
2nd harmonic level	< -17dBc
Output VSWR	1.5:1 max; 50 Ohms
Output waveform	Sinusoidal
Rise / Fall time Analog	4 nsec typ., 8 nsec max (freq. ≥ 200 MHz)
Rise / Fall time Digital	25 nsec max
Contrast ratio	50 dB min
Analog input voltage	0 – 1 V
Analog input impedance	50 Ohms
Digital input voltage	TTL (5V) or LVTTTL (3.3V) (VIH = 2.0V, VIL = 0.8V)
Digital input impedance	10k Ohms
Frequency stability	+/- 5 ppm over temp
Frequency accuracy	+/- 5 ppm
Jitter	0.3 psec typ.
Phase Noise	-105 dBc/Hz at 10kHz offset
Thermal management	Conduction cooled
Operating temperature range	+10°C to +60°C (referenced to mounting tab) Unit will operate down to 0°C, in a non-condensing environment

Document

04/10/2024

Control

Table A

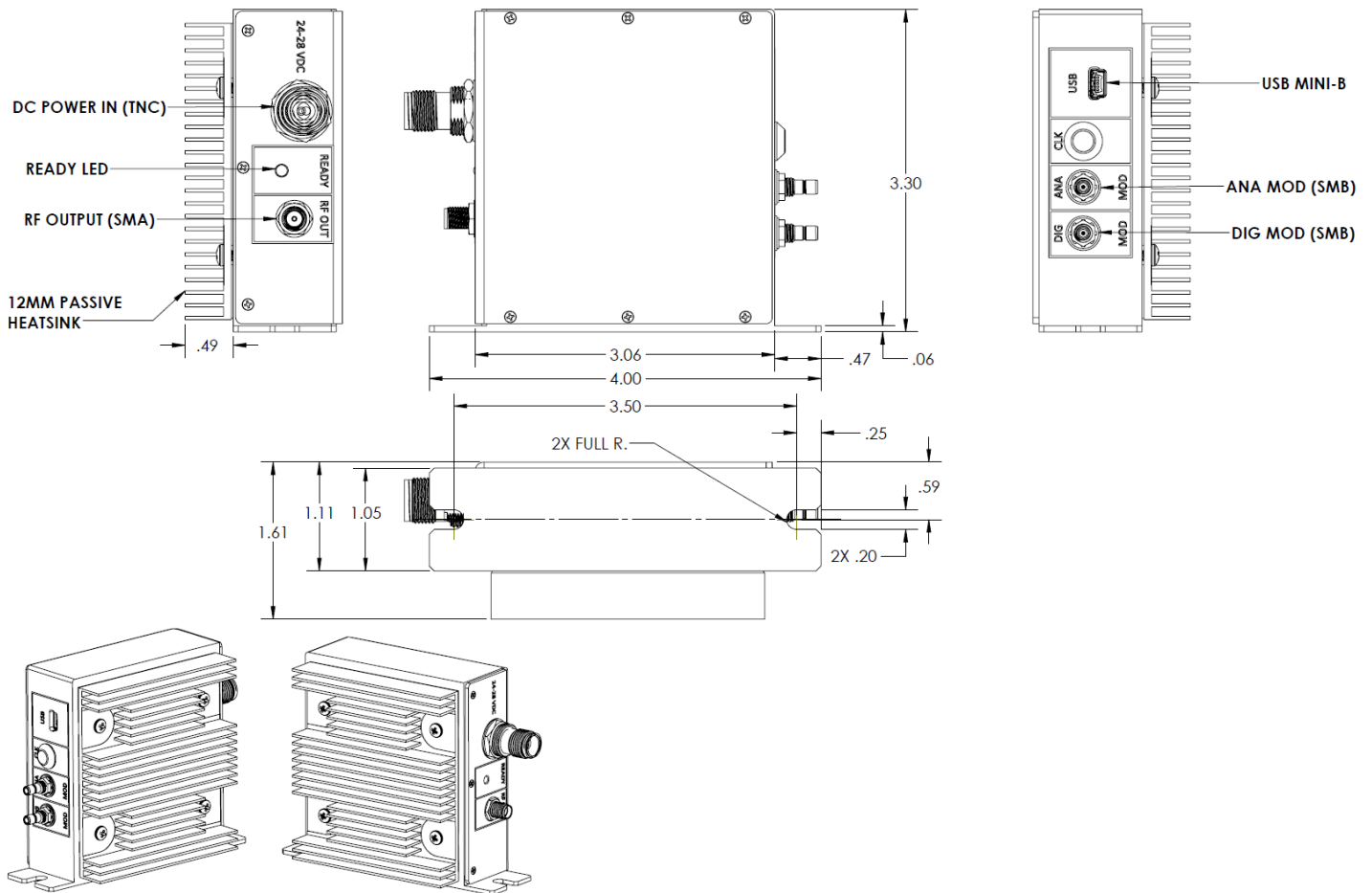
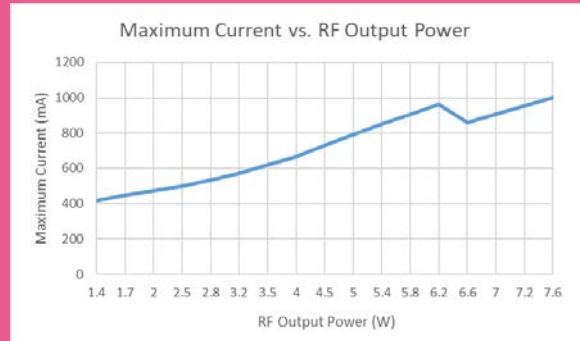
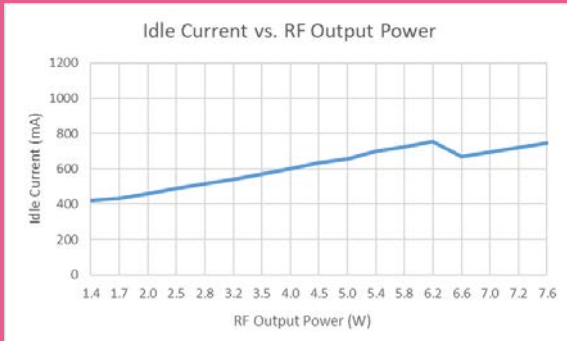
Part Number	Freq Center (MHz)	Modulation	RF Power (Watts)	Model
97-03941-01	120	Combined	3.5	1120AFP-AD-3.5
97-03941-02	160	Combined	6	1160AFP-AD-6.0
97-03941-03	175	Combined	4.5	1175AFP-AD-4.5
97-03941-04	110	Digital	3.5	1110AFP-D-3.5
97-03941-05	110	Analog	5	1110AFP-A-5.0
97-03941-06	150	Combined	3	1150AFP-AD-3.0
97-03941-07	110	Combined	3	1110AFP-AD-3.0
97-03941-08	175	Combined	3.5	1175AFP-AD-3.5
97-03941-09	110	Digital	3	1110AFP-D-3.0
97-03941-10	110	Analog	4	1110AFP-A-4.0
97-03941-11	110	Combined	8	1110AFP-AD-8.0
97-03941-12	110	Digital	5	1110AFP-D-5.0
97-03941-13	165	Combined	4	1165AFP-AD-4.0
97-03941-14	110	Combined	4	1110AFP-AD-4.0
97-03941-15	110	Combined	5	1110AFP-AD-5.0
97-03941-16	120	Combined	3	1120AFP-AD-3.0
97-03941-17	165	Analog	4	1165AFP-A-4.0
97-03941-18	165	Digital	4	1165AFP-D-4.0

Document

04/10/2024

Control

Table B



Document

04/10/2024

Control